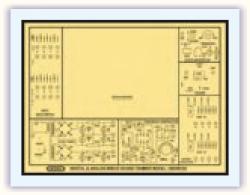


This trainer has been designed with a view to verify truth table of logic gates and address and to construct small Analog and digital circuits and perform various experiments.



SPECIFICATIONS

- 1. Breadboard provided
- 2. Built in Power Supply
- 3. Clock generator-TTL
- 4. Pulsar
- 5. LED output level indicators
- 6. Logic input switches
- 7. Built in Sine wave Generator
- 8. Standard Accessories

- 1 strips 175mm long with 640 tie points
- + 5 V, 1A. +/-15 V, 250mA Regulated Power supply
- Variable from 0.1 Hz to 1 KHz.
- 1 No. provided to apply pulses.
- 8 Nos.

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- 8 Nos.
 - 1. Trainer Manual.
 - 2. Connecting Wires.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.